

invention enables a low cost implementation of electronic applications on smaller substrates.

V. Conclusion

[0098] While various embodiments of the present invention have been described above, it should be understood that they have been presented by way of example only, and not limitation. For example, the present invention may be used for applications other than diplexers. One such application is an impedance matching network.

[0099] In addition, various aspects of the present invention are described above in the context of a plurality of spiral conductive patterns disposed on adjacent surfaces. However, the present invention may include surfaces that do not include spiral conductive patterns between surfaces having spiral conductive patterns.

[0100] Furthermore, the invention is not limited to the exemplary implementations described above having two, three, and four layers of spiral conductive patterns. In fact, any number of such layers may be employed. Also, even though FIGs. 9-10 show spiral patterns having approximately one and a half turns, the present invention may employ spiral patterns having any number of turns.

[0101] Finally, it will be understood by those skilled in the art that various changes in form and details may be made therein without departing from the spirit and scope of the invention as defined in the appended claims. Thus, the breadth and scope of the present invention should not be limited by any of the above-described exemplary embodiments, but should be defined only in accordance with the following claims and their equivalents.